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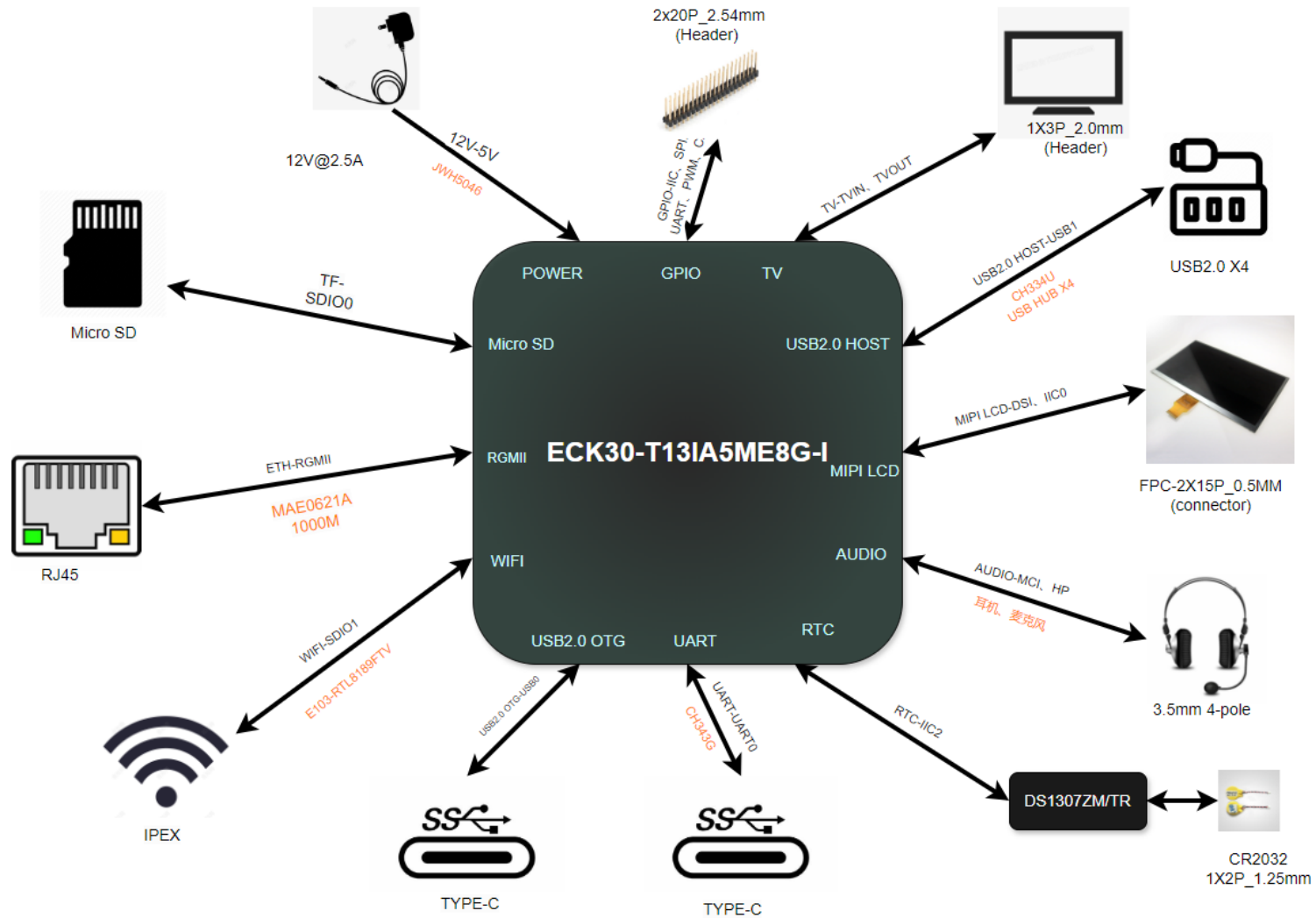
30012

P2024-EC007

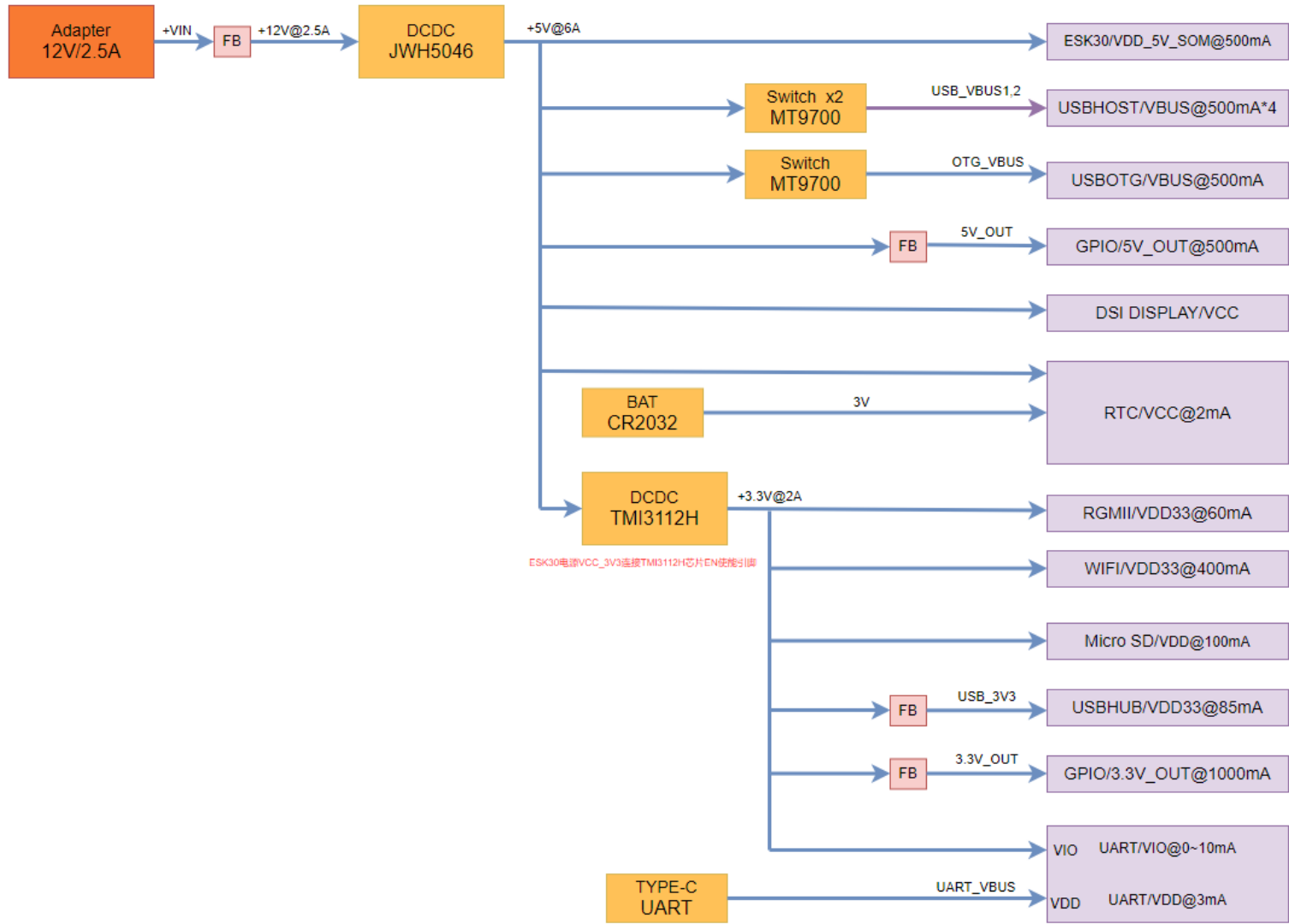
Revision History


V1.0-2024.09-23-WJ
1、初始版本。

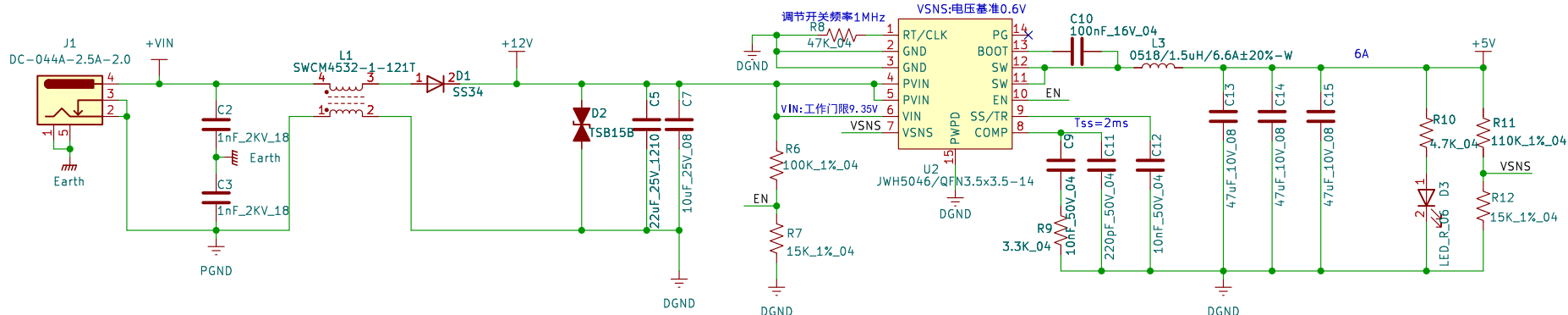
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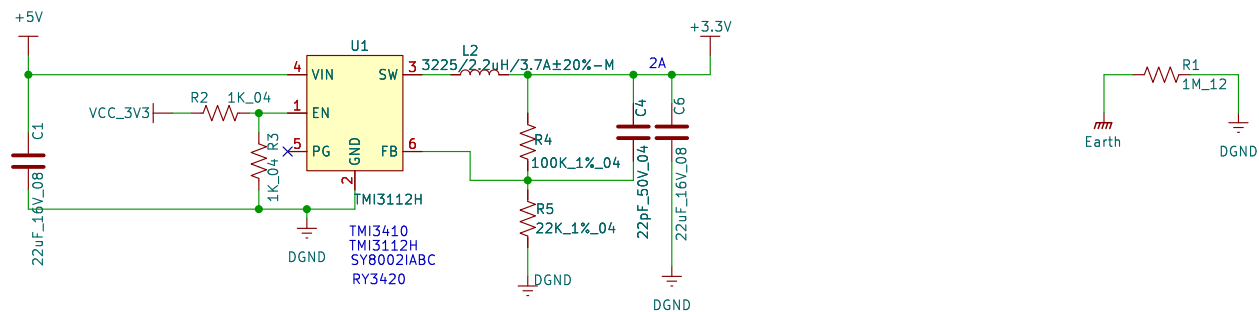
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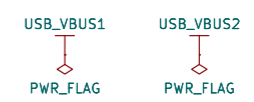
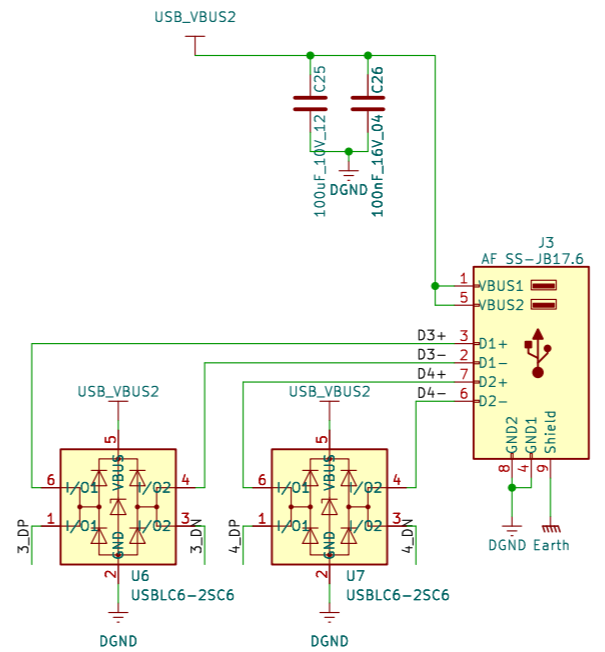
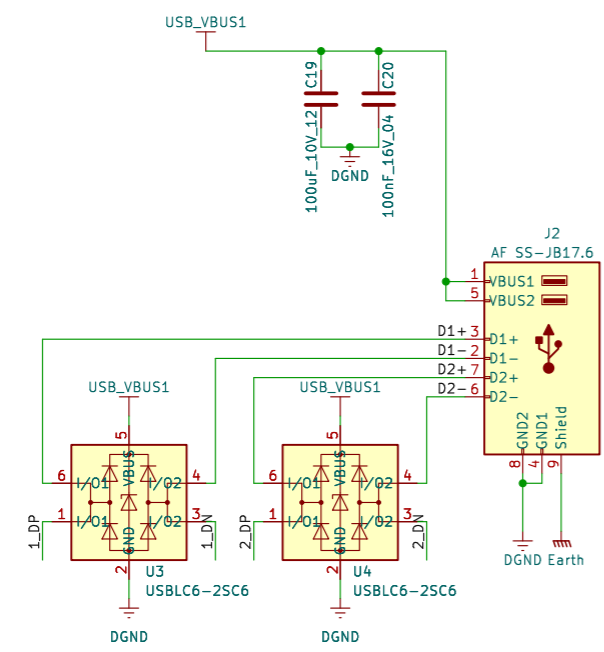
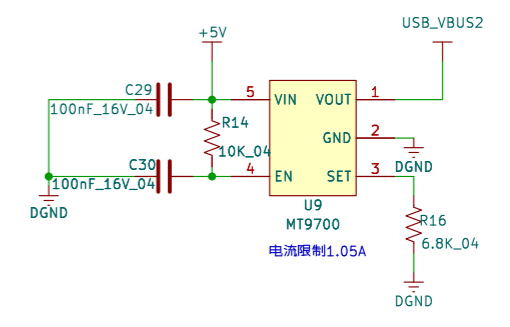
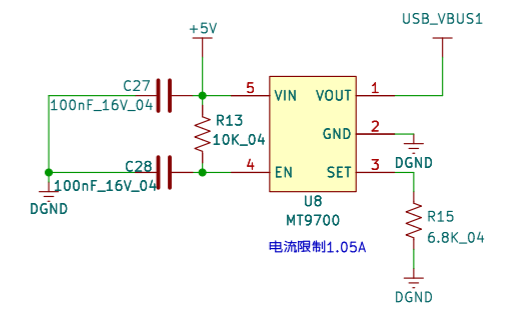
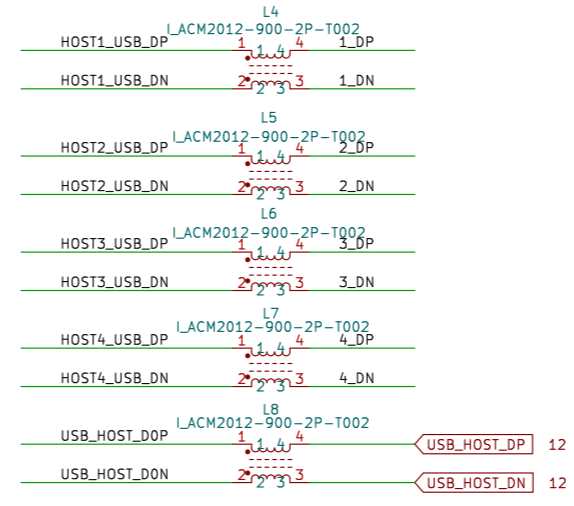
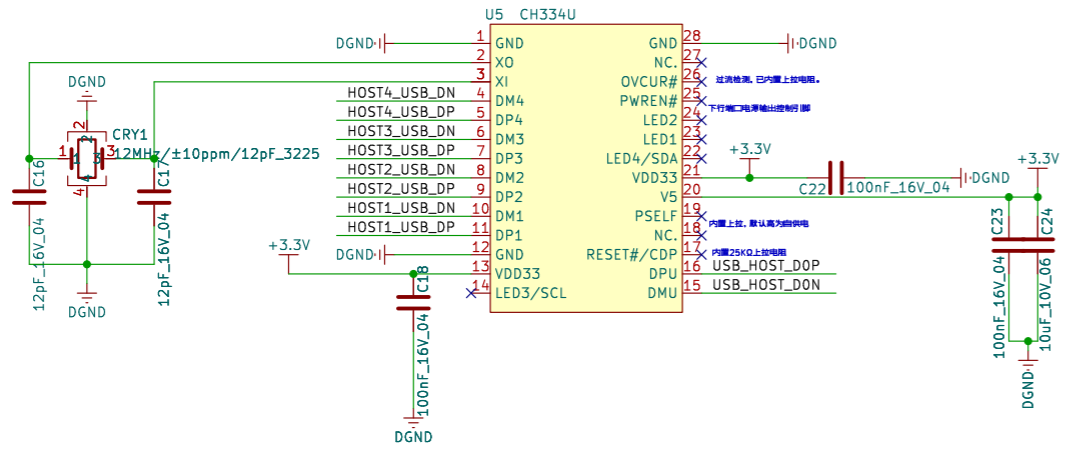
12V TO 5V



5V TO 3.3V

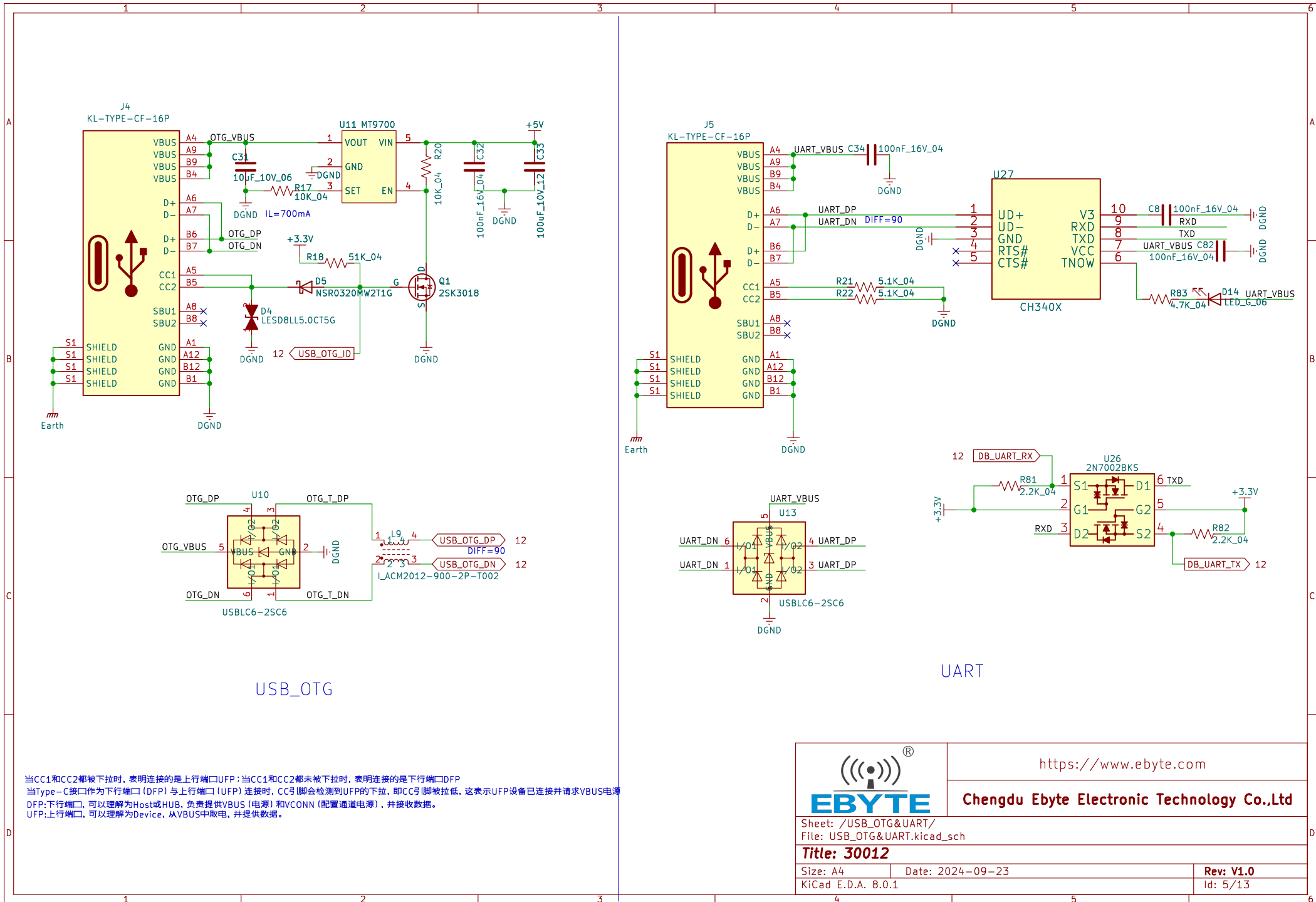


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USB_HOST


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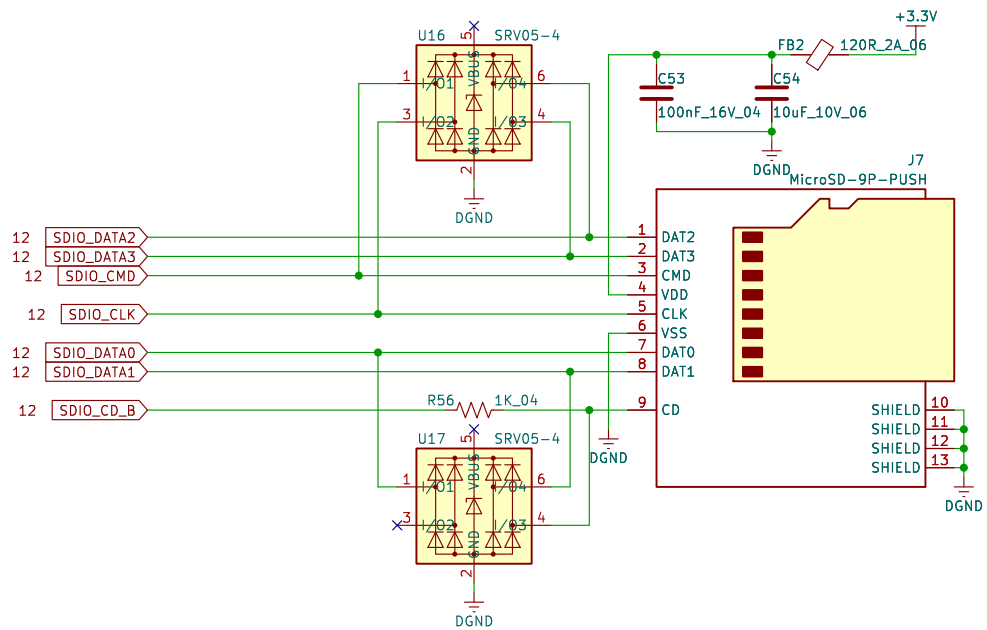


USB_OTG

UART

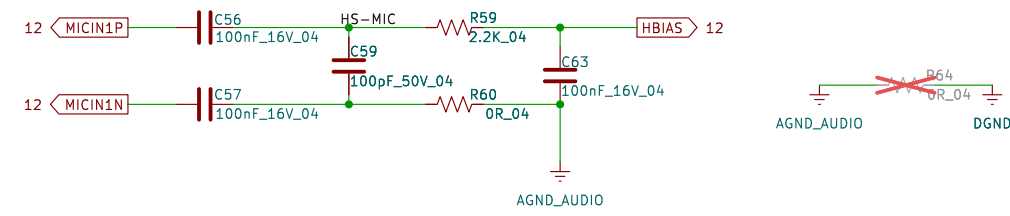
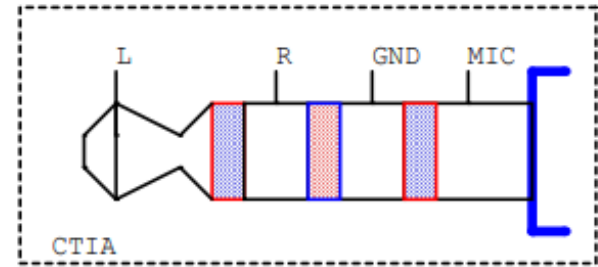
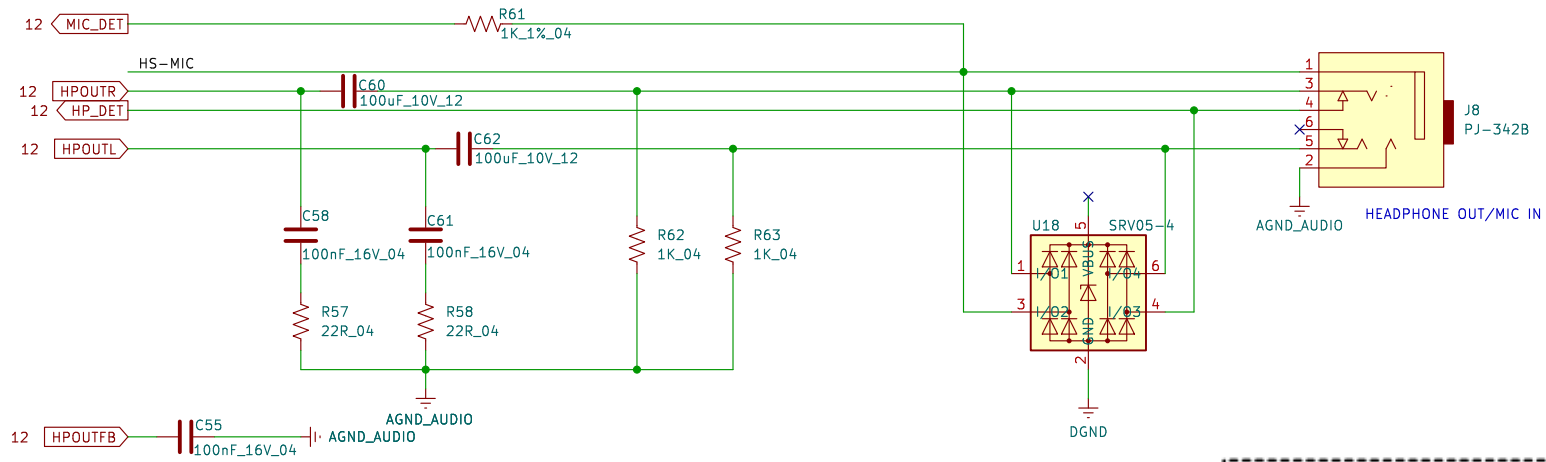
当CC1和CC2都被下拉时，表明连接的是上行端口UFP；当CC1和CC2都未被下拉时，表明连接的是下行端口DFP
 当Type-C接口作为下行端口（DFP）与上行端口（UFP）连接时，CC引脚会检测到UFP的下拉，即CC引脚被拉低，这表示UFP设备已连接并请求VBUS电源
 DFP:下行端口，可以理解为主机或HUB，负责提供VBUS（电源）和VCONN（配置通道电源），并接收数据。
 UFP:上行端口，可以理解为客户设备，从VBUS中取电，并提供数据。

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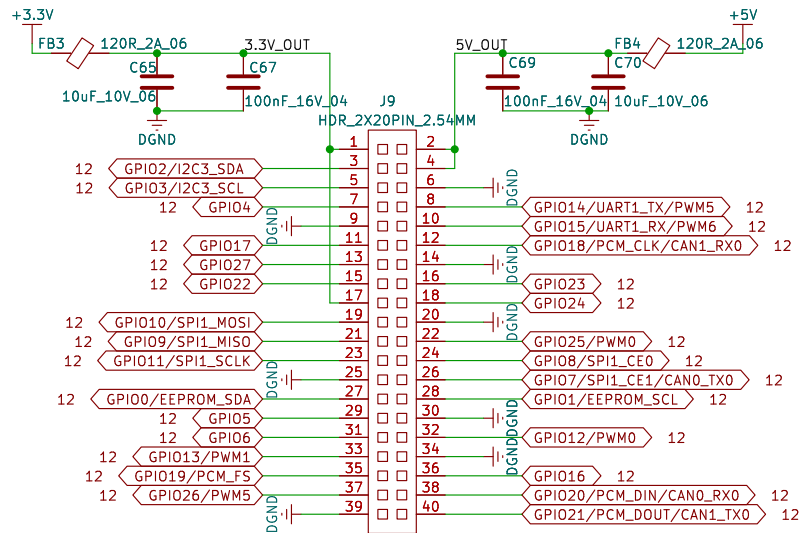
MICRO SD

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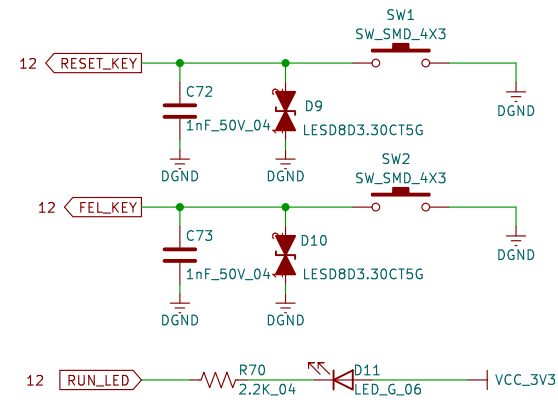


AUDIO

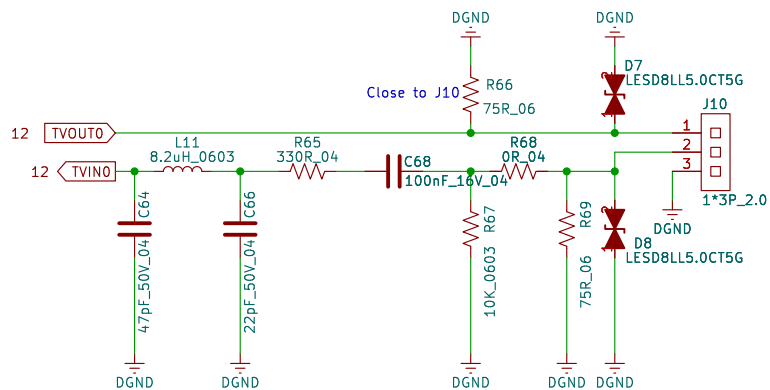
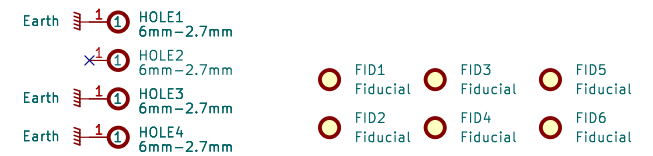
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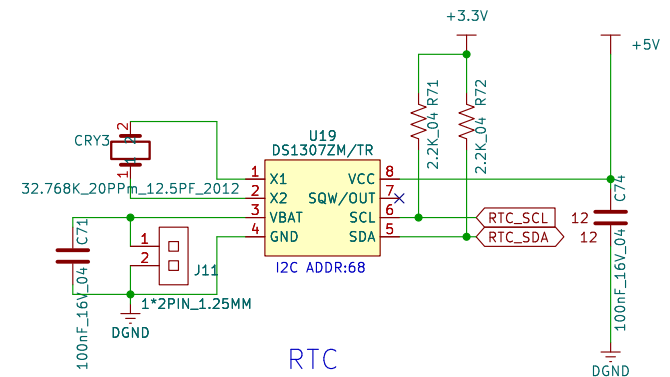
GPIO



LED&KEY



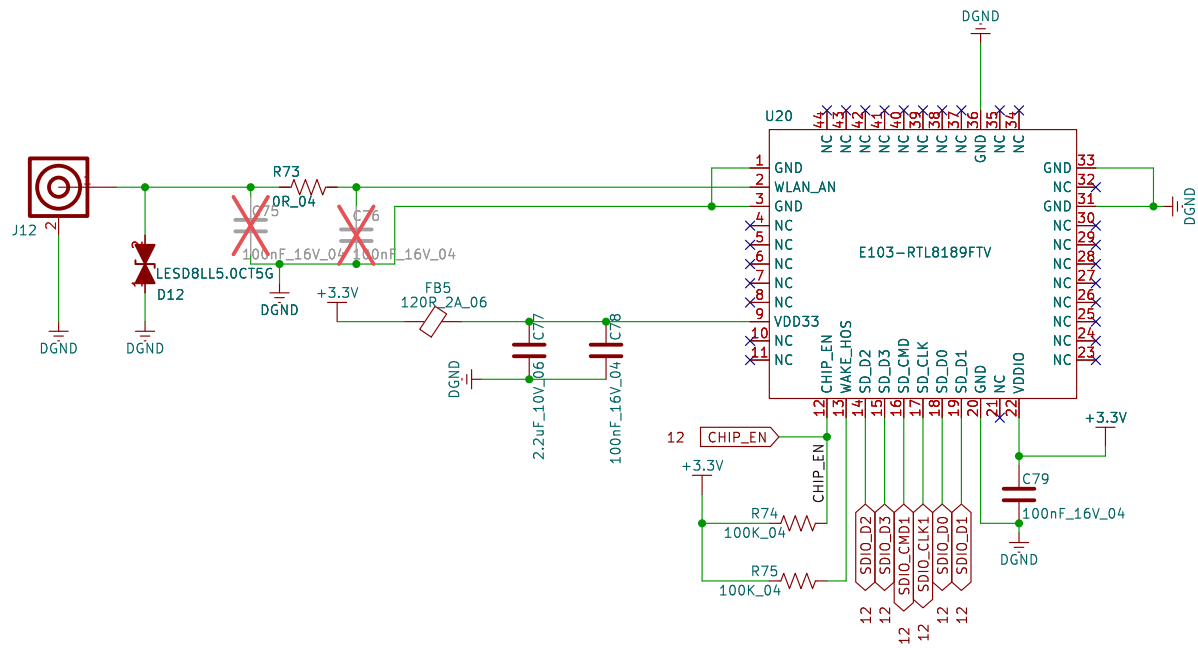
模拟信号接口



RTC

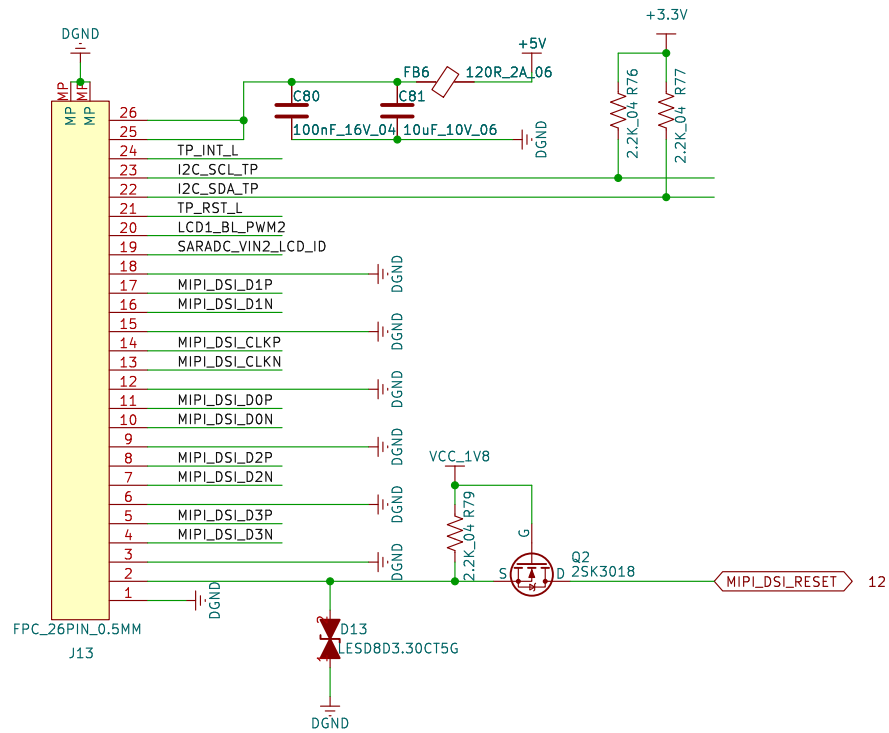

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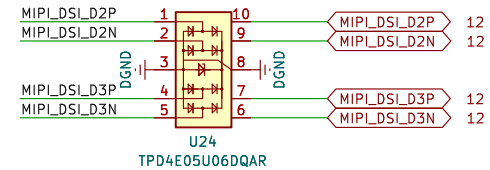
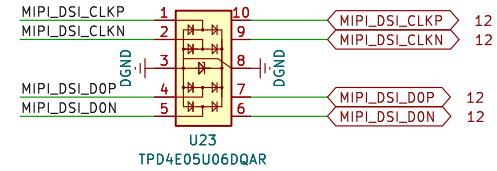
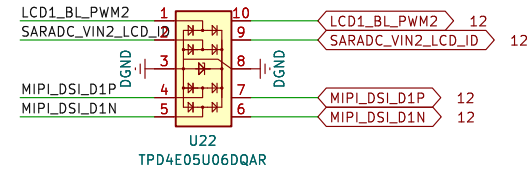
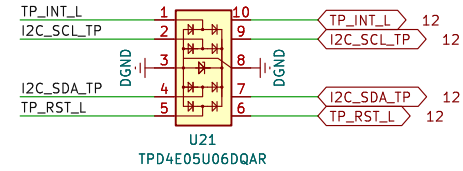
WiFi

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MIPI_DSI

VCC_1V8
PWR_FLAG



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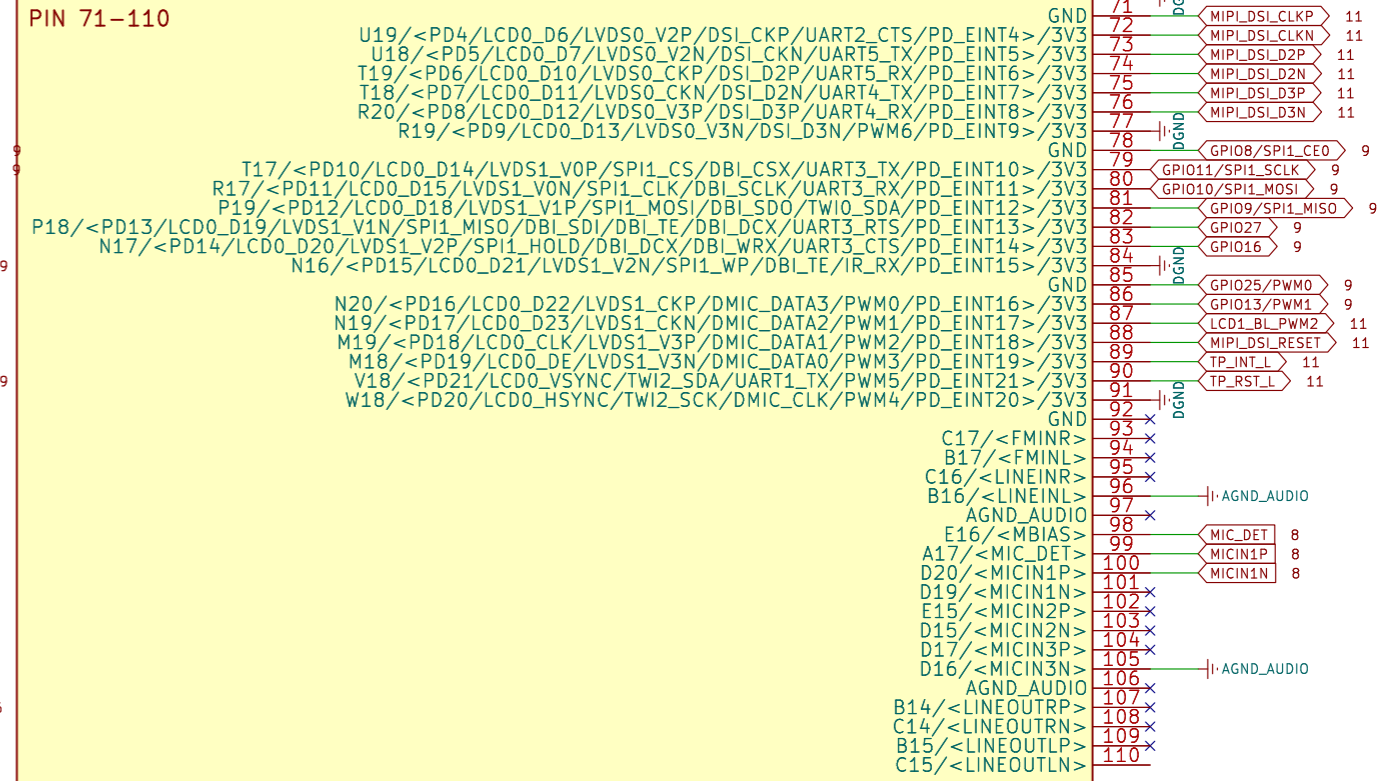
P1A
ECK30_T13IA5ME8G_I

P1C
ECK30_T13IA5ME8G_I

PIN 1-40



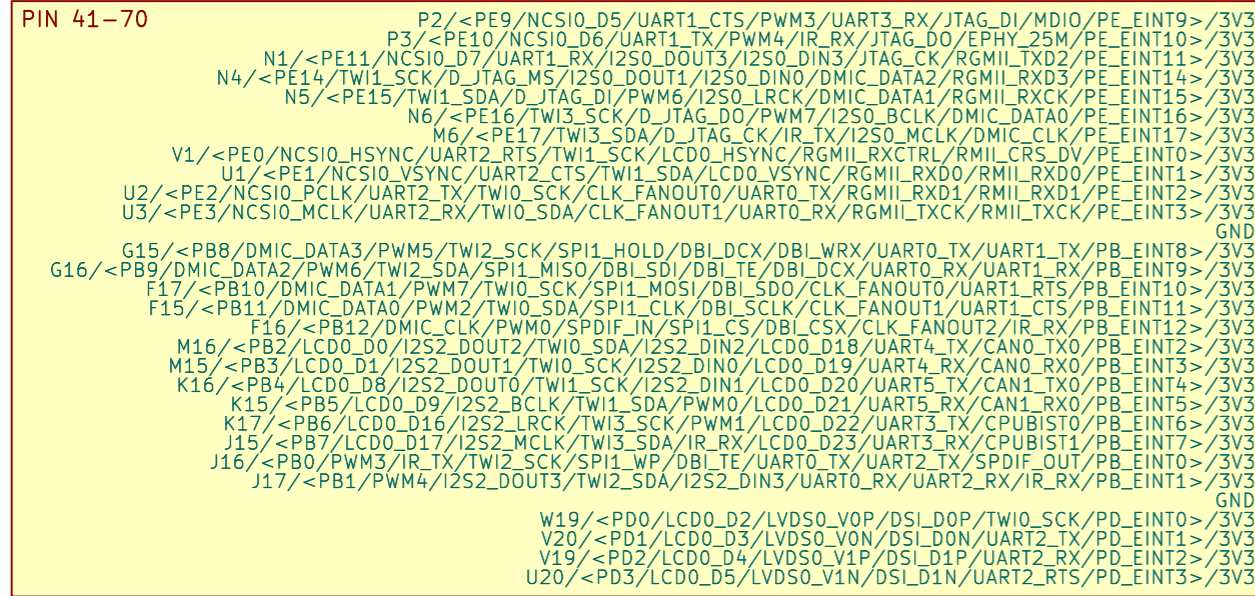
PIN 71-110



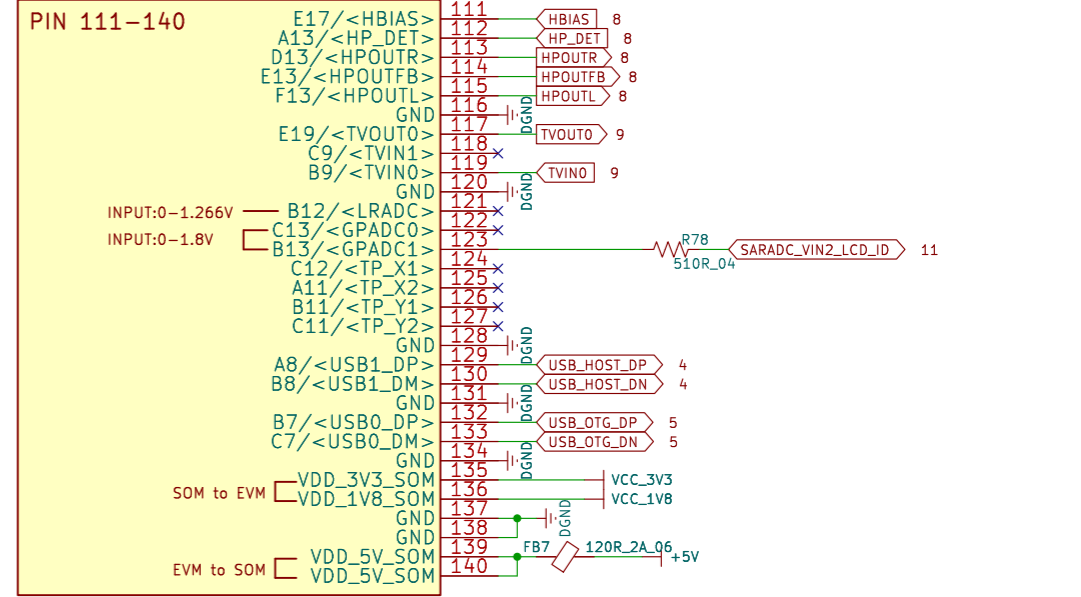
P1B
ECK30_T13IA5ME8G_I


P1D
ECK30_T13IA5ME8G_I

PIN 41-70



PIN 111-140



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